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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants:	Rajesh Sundaram, et al.	§	Group Art Unit:	2827
		§		
Serial No.:	10/713,841	§		
		§	Examiner:	Thong Quoc Le
Filed:	November 13, 2003	§		
		§		
For:	Negatively Biasing	§	Atty. Dkt. No.:	ITL.1062US (P17921)
	Deselected Memory Cells	§		
		§	Assignee:	Intel Corporation

Mail Stop Amendment  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**REPLY TO OFFICE ACTION MAILED APRIL 25, 2005**

Sir:

In response to the Office Action mailed April 25, 2005, please amend the above-referenced patent application as follows:

**Amendments to the Claims** begin on page 2 of this paper.

**Remarks/Arguments** begin on page 6 of this paper.

Date of Deposit: June 30, 2005

I hereby certify under 37 CFR 1.8(a) that this correspondence is being deposited with the United States Postal Service as **first class mail** with sufficient postage on the date indicated above and is addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

  
Jennifer Suarez